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| --- | --- | --- | --- | --- | --- | --- |
| **ZCZ Ball  Number** | **CONTROL\_MODULE REGISTERS  Pin Name Offset** | | **Signal Name** | **Mux mode** | **Type** | **Description** |
| A15 | XDMA\_EVT\_INTR0 | 0x9b0h | pr1\_pru1\_pru\_r31\_16 | 5 | I | PRU1 Data In Capture Enable |
| D14 | XDMA\_EVT\_INTR1 | 0x9B4h | pr1\_pru0\_pru\_r31\_16 | 5 | I | PRU0 Data In Capture Enable |
| U9 | GPMC\_CSn1 | 0x880h | pr1\_pru1\_pru\_r30\_12 pr1\_pru1\_pru\_r31\_12 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| V9 | GPMC\_CSn2 | 0x884h | pr1\_pru1\_pru\_r30\_13 pr1\_pru1\_pru\_r31\_13 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| T12 | GPMC\_AD12 | 0x830h | pr1\_pru0\_pru\_r30\_14 | 6 | O | PRU0 Data Out |
| R12 | GPMC\_AD13 | 0x834h | pr1\_pru0\_pru\_r30\_15 | 6 | O | PRU0 Data Out |
| V13 | GPMC\_AD14 | 0x838h | pr1\_pru0\_pru\_r31\_14 | 6 | I | PRU0 Data In |
| U13 | GPMC\_AD15 | 0x83Ch | pr1\_pru0\_pru\_r31\_15 | 6 | I | PRU0 Data In |
| G17 | MMC0\_CLK | 0x900h | pr1\_pru0\_pru\_r30\_12 pr1\_pru0\_pru\_r31\_12 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| G18 | MMC0\_CMD | 0x904h | pr1\_pru0\_pru\_r30\_13 pr1\_pru0\_pru\_r31\_13 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| G16 | MMC0\_DAT0 | 0x8FCh | pr1\_pru0\_pru\_r30\_11 pr1\_pru0\_pru\_r31\_11 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| G15 | MMC0\_DAT1 | 0x8F8h | pr1\_pru0\_pru\_r30\_10 pr1\_pru0\_pru\_r31\_10 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| F18 | MMC0\_DAT2 | 0x8F4h | pr1\_pru0\_pru\_r30\_9 pr1\_pru0\_pru\_r31\_9 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| F17 | MMC0\_DAT3 | 0x8F0h | pr1\_pru0\_pru\_r30\_8 pr1\_pru0\_pru\_r31\_8 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| E16 | UART0\_TXD | 0x974h | pr1\_pru1\_pru\_r30\_15 pr1\_pru1\_pru\_r31\_15 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| E15 | UART0\_RXD | 0x970h | pr1\_pru1\_pru\_r30\_14 pr1\_pru1\_pru\_r31\_14 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| D15 | UART1\_TXD | 0x984h | pr1\_pru0\_pru\_r31\_16 | 6 | I | PRU0 Data In Capture Enable |
| D16 | UART1\_RXD | 0x980h | pr1\_pru1\_pru\_r31\_16 | 6 | I | PRU1 Data In Capture Enable |
| R1 | LCD\_DATA0 | 0x8A0h | pr1\_pru1\_pru\_r30\_0 pr1\_pru1\_pru\_r31\_0 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| R2 | LCD\_DATA1 | 0x8A4h | pr1\_pru1\_pru\_r30\_1 pr1\_pru1\_pru\_r31\_1 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| R3 | LCD\_DATA2 | 0x8A8h | pr1\_pru1\_pru\_r30\_2 pr1\_pru1\_pru\_r31\_2 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| R4 | LCD\_DATA3 | 0x8ACh | pr1\_pru1\_pru\_r30\_3 pr1\_pru1\_pru\_r31\_3 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| T1 | LCD\_DATA4 | 0x8B0h | pr1\_pru1\_pru\_r30\_4 pr1\_pru1\_pru\_r31\_4 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| T2 | LCD\_DATA5 | 0x8B4h | pr1\_pru1\_pru\_r30\_5 pr1\_pru1\_pru\_r31\_5 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| T3 | LCD\_DATA6 | 0x8B8h | pr1\_pru1\_pru\_r30\_6  pr1\_pru1\_pru\_r31\_6 | 5  6 | OI | PRU1 Data Out  PRU1 Data In |
| T4 | LCD\_DATA7 | 0x8BCh | pr1\_pru1\_pru\_r30\_7 pr1\_pru1\_pru\_r31\_7 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| V5 | LCD\_PCLK | 0x8E8h | pr1\_pru1\_pru\_r30\_10 pr1\_pru1\_pru\_r31\_10 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| U5 | LCD\_VSYNC | 0x8E0h | pr1\_pru1\_pru\_r30\_8 pr1\_pru1\_pru\_r31\_8 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| R5 | LCD\_HSYNC | 0x8E4h | pr1\_pru1\_pru\_r30\_9 pr1\_pru1\_pru\_r31\_9 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| R6 | LCD\_AC\_BIAS\_EN | 0x8ECh | pr1\_pru1\_pru\_r30\_11  pr1\_pru1\_pru\_r31\_11 | 5 6 | O I | PRU1 Data Out PRU1 Data In |
| A14 | McASP0\_AHCLKX | 0x9ACh | pr1\_pru0\_pru\_r30\_7 pr1\_pru0\_pru\_r31\_7 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| A13 | McASP0\_ACLKX | 0x990h | pr1\_pru0\_pru\_r30\_0 pr1\_pru0\_pru\_r31\_0 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| B13 | McASP0\_FSX | 0x994h | pr1\_pru0\_pru\_r30\_1 pr1\_pru0\_pru\_r31\_1 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| D12 | McASP0\_AXR0 | 0x998h | pr1\_pru0\_pru\_r30\_2 pr1\_pru0\_pru\_r31\_2 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| C12 | McASP0\_AHCLKR | 0x99Ch | pr1\_pru0\_pru\_r30\_3 pr1\_pru0\_pru\_r31\_3 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| B12 | McASP0\_ACLKR | 0x9A0h | pr1\_pru0\_pru\_r30\_4 pr1\_pru0\_pru\_r31\_4 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| C13 | McASP0\_FSR | 0x9A4h | pr1\_pru0\_pru\_r30\_5 pr1\_pru0\_pru\_r31\_5 | 5 6 | O I | PRU0 Data Out PRU0 Data In |
| D13 | McASP0\_AXR1 | 0x9A8h | pr1\_pru0\_pru\_r30\_6 pr1\_pru0\_pru\_r31\_6 | 5 6 | O I | PRU0 Data Out PRU0 Data In |